**specimen h - ocr** - section c  $\tilde{A} \not \in \hat{A} \in \hat{A}$  module p6 . 12. sally does some experiments with electricity. (a) look at the diagram. the resistance wire is used to control the bulb. (i) sally puts the slider at position a. the bulb lights up. ... unit b752: physics modules p4, p5, p6 (higher tier) duration: 1 hour 30 minutes.

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csc 2405 â€Â" cpu scheduling exercises - exercise 2 [cpu scheduling, no i/o] consider seven

processes p 1, p 2,  $\tilde{A}\phi\hat{A}\in\hat{A}$ , p 7 with arrival times and cpu burst times as follows: process p1 p2 p3 p4 p5 p6 p7 arrival time 2 - $\tilde{A}\check{Z}\hat{A}\mu$ 4 57 9 1 16 cpu burst time 3 2 1 4 2 6 8 here  $\tilde{A}\phi\hat{A}\in\hat{A}\otimes 2-\tilde{A}\check{Z}\hat{A}\mu\tilde{A}\phi\hat{A}\in\hat{A}$ • indicates that p1 has arrived just before time unit 2, and similarly for the others. assume that, when joining the ready queue, (new or existing) processes ...

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